REMARKS/ARGUMENTS

In response to the Examiner's final Office Action of July 25, 2007 the Applicant respectfully submits the accompanying Amendment of the claims and the below Remarks.

Regarding Amendments

In the Amendments:

claim 1 is amended to clarify that the tamper detection line is arranged to pass directly over each data bit of the non-volatile memory so as to obscure the operation thereof. Support for this amendment can be found in paragraphs [6648]-[6669] of the present specification; and

claims 2-10 are unchanged.

It is respectfully submitted that the above amendments do not add new matter to the present application, nor any new issues to the prosecution of the present application.

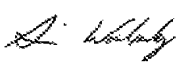
Regarding 35 USC 103(a) Rejections

It is respectfully submitted that the subject matter of above-described amended independent claim 1, and claims 2-10, is not taught or suggested by previously cited Hameau (and Pires) in view of newly cited Silverbrook et al. (US 6,246,970), because, as the Examiner admits, Hameau and Pires do not teach or suggest tamper detection lines and because Silverbrook merely discloses covering random source generator circuitry with tamper detection lines (see col. 6, lines 34-38) and does not further disclose or suggest that such tamper detection lines can be used to obscure operation of non-volatile memory by passing directly above each data bit thereof, as is required by the claimed invention.

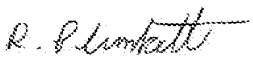
It is respectfully submitted that all of the Examiner's rejections have been traversed. Accordingly, it is submitted that the present application is in condition for allowance and reconsideration of the present application is respectfully requested.

Very respectfully,

Applicant/s:



Simon Robert Walmsley



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